



>> Tuesday June 17th

8:45	Opening	
9:00	Invited Speaker Session chair: J.P. Rosen An Invitation to Ada 2005 <i>Pascal Leroy, Rational Software Corp., France</i>	
10:00	Exhibitions and Coffee break	
	<i>Session "Ravenscar"</i> Session chair: J. Blieberger	<i>Session "Language Issues"</i> Session chair: J. Barnes
11:00	High Integrity Ravenscar <i>Peter Amey & Brian Dobbing, UK</i>	Booch's Ada vs. Liskov's Java: Two Approaches to Teaching Software Design <i>Ehud Lamm, Israel</i>
11:30	Adding Temporal Annotations and Associated Verification to Ravenscar Profile <i>Alan Burns & Tse-Min Lin, UK</i>	A Comparison of the Asynchronous Transfer of Control Features in Ada and the Real-Time Specification for Java <i>Benjamin M. Brosgol & A. Wellings, USA, UK</i>
12:00	Towards Static Verification of Real-Time Performance: The Case of the GOCE Platform Application Software <i>Niklas Holsti & Thomas Långbacka, UK</i>	Exposing Memory Corruption and Finding Leaks: Advanced Mechanisms in Ada <i>Emmanuel Briot, Franco Gasperoni, Robert Dewar, Dirk Craeynest & Philippe Waroquiers, France - USA - Belgium</i>
12:00	Lunch and Exhibitions	
	<i>Session "Static Analysis"</i> Session chair: K. Lundkvist	<i>Vendor Session 1</i> Session chair: D. Craeynest
14:30	Busy Wait Analysis <i>Johann Blieberger, Bernd Burgstaller & Bernhard Scholz, Austria</i>	ACT <i>Franco Gasperoni</i>
15:00	Eliminating Redundant Range Checks in GNAT Using Symbolic Evaluation <i>Bernd Burgstaller, Austria</i>	GreenHills Software
15:30	Quasar : A New Tool for Concurrent Ada Programs Analysis <i>S. Evangelista, Claude. Kaiser, Jean-François. Pradat-Peyre & P. Rousseau, France</i>	CS Using the NEPTUNE Technology to Check and Document UML Models
16:00	Exhibitions and Coffee break	
17:00	Ada-Europe General Assembly	
18:00		
18:30	Reception in Toulouse's Town Hall	