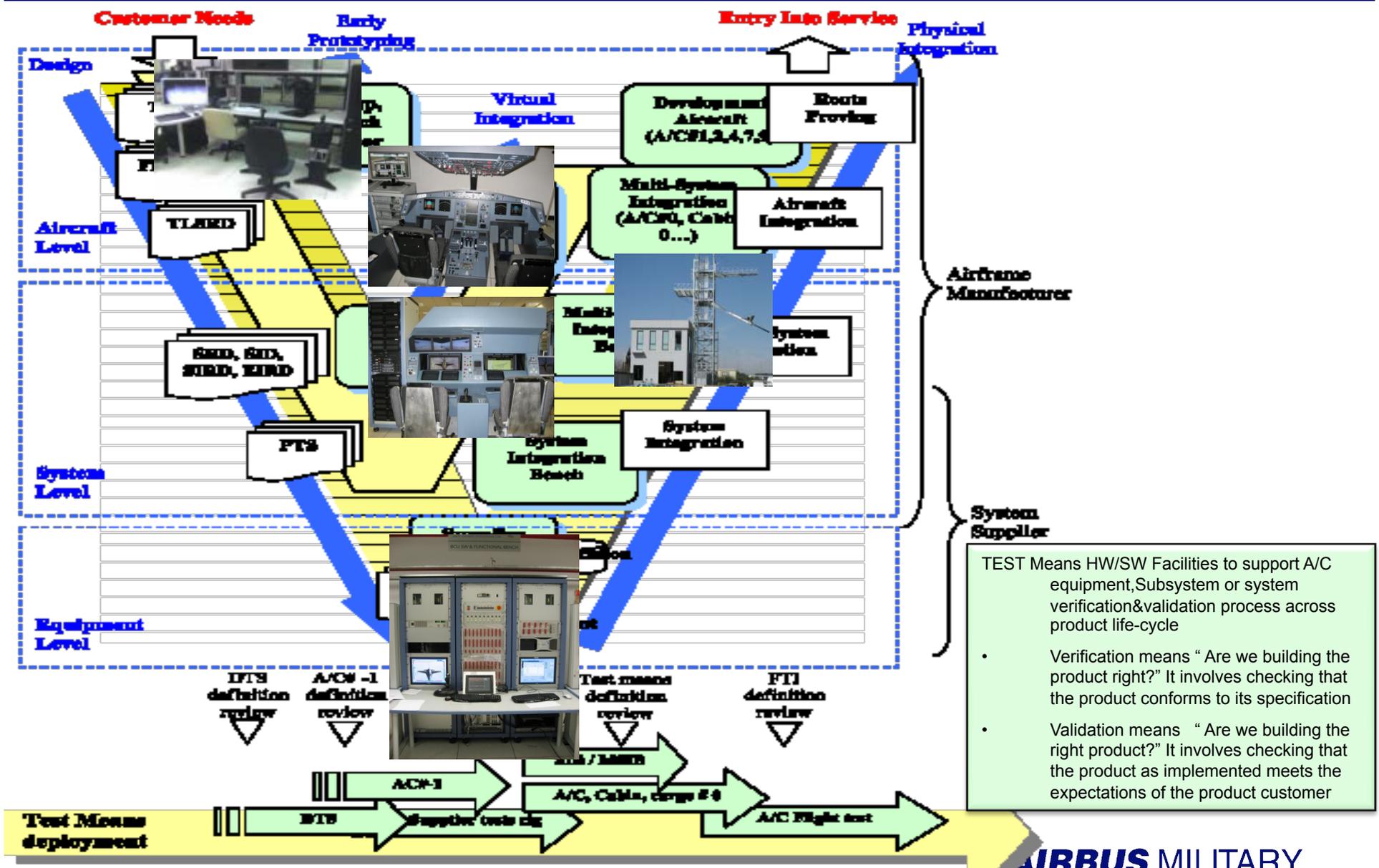


Ada95 and GNATPRO in AIM generic TEST SYSTEM

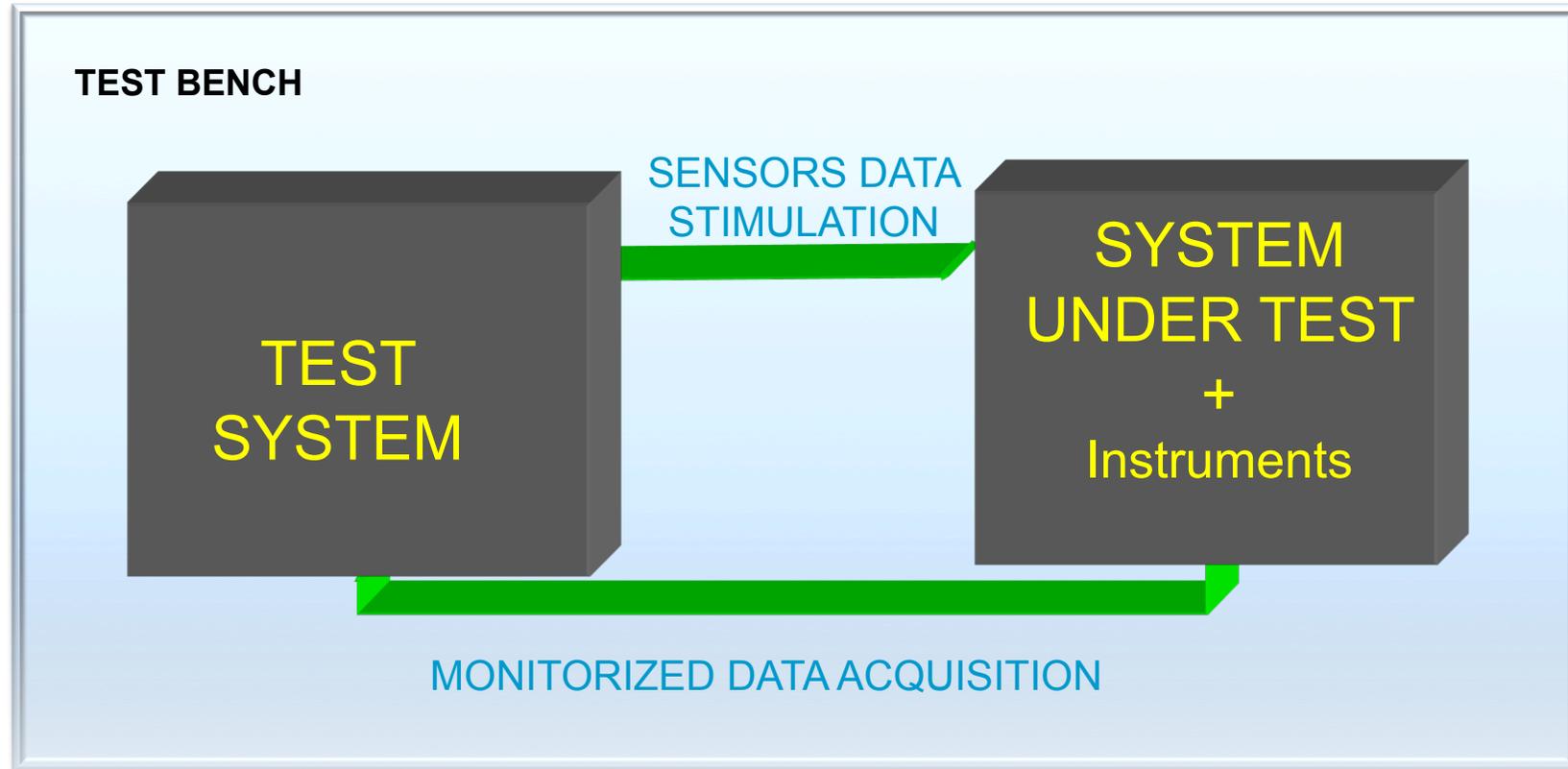


A/C electronics systems life cycle



AIRBUS MILITARY

SEAS Overview



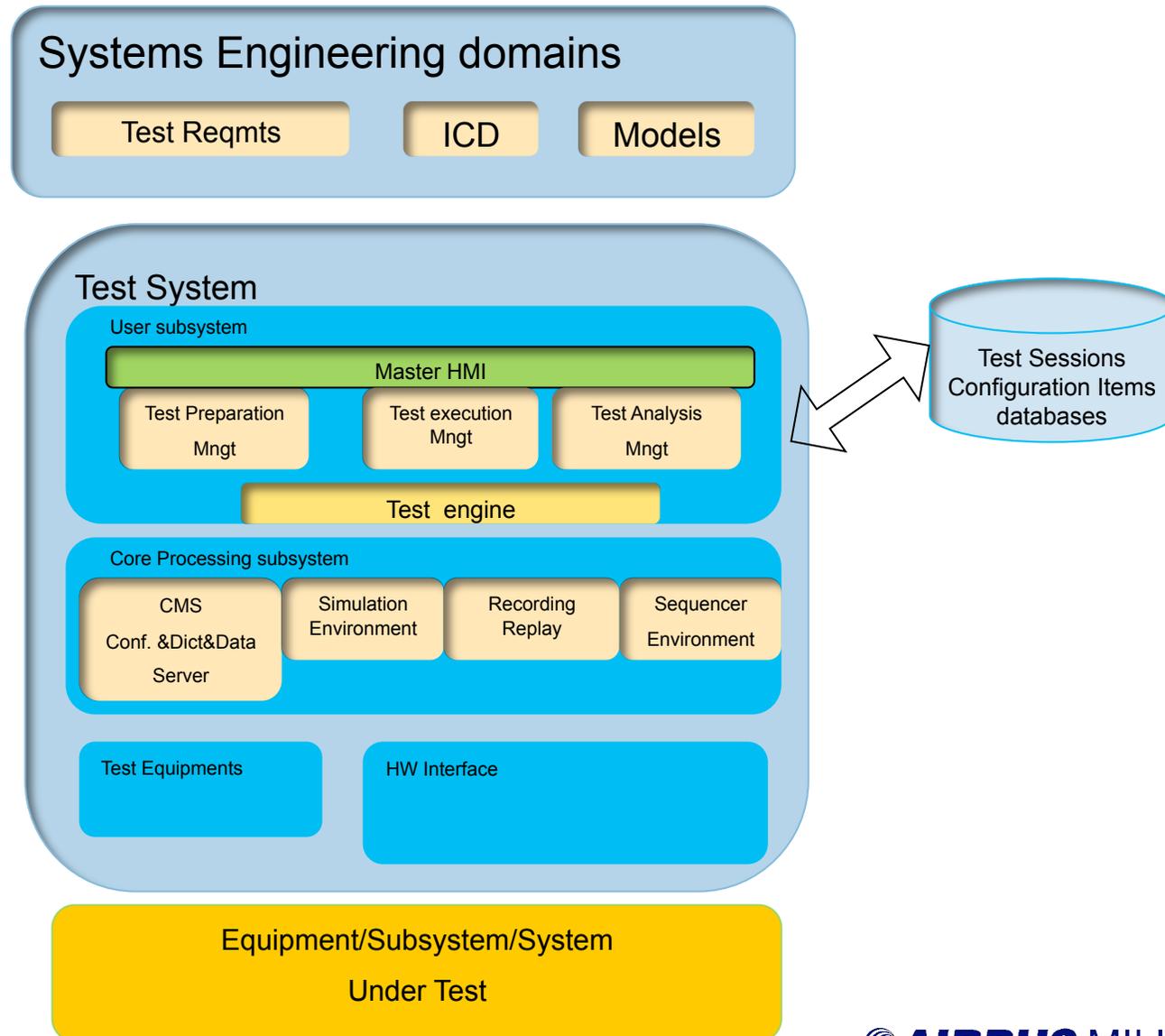
Stimulation, Acquisition and Simulation System (SEAS)

is a modular generic distributed HW/SW items used to build test facilities
(Engineering Simulator, SW Benches, Functional test benches and Target Rigs)

SEAS MOSA principles

- Modular, Open, distributed and scalable architecture widely used with minimal changes from Desk Top Simulators to target rigs
- Reconfigurability, Portability, Interoperability, Reusability, Scalability, Reliability and Maintainability
 - Simulation environment multi-platform and multi-OS
 - *Generic SW components are written in Ada95 to improve reliability*
- Multi-language support for simulations and bench specific SW (Ada95, C, C++, Java, VB, Fortran, ...)
- VME, PCI, PCI-e form factors support for processors, I/O boards and USB for multimedia devices
- Open integration to:
 - COTS industrial test tools by flexible adapters
 - COTS Avionics and non-avionics I/F cards (A429, AFDX, 1553, EFEX, CAN, Analogues, Digital, Discretes, Ethernet, Shared Memory, High Speed data links)
 - COTS processors
 - COTS HMI tools (GTKADA, VI, process control...)
 - COTS multimedia products
 - COTS instruments (PXI, LXI, Ethernet)

SEAS Context Diagram



Test Sessions

- Test Session: DEFAULT_TEST
- Test Session: MIP1_VB3C
- Version: 0
- Version: 1
 - Description: MIP1 with PSD definitions
 - Aircraft Revision ID: N/A
 - Server: OSK
 - Bench Revision: 3.3.0.2
 - Hw Config: HW_CONF02_SEAS.VML
 - Signal Definitions: APS_ASP_PSD_SIGNAL_DEF.VML
 - Scal Definitions: APS_MIP1_U83_C_PSD_PROCESS.VML
 - Process Definition: APS_ASP_PSD_BUSDEF.VML
 - Bus Definition: APS_ASP_PSD_BUSDEF.VML
 - Simulations:
 - Data Visualisation Panels
 - Recording Definitions:
 - Recording: MIP1_REC
 - Recording: MIP1_SPEED_EST_VIP
 - Recording: MIP1_START_INP_UITS

Test MIP1_VB3C v1 loaded

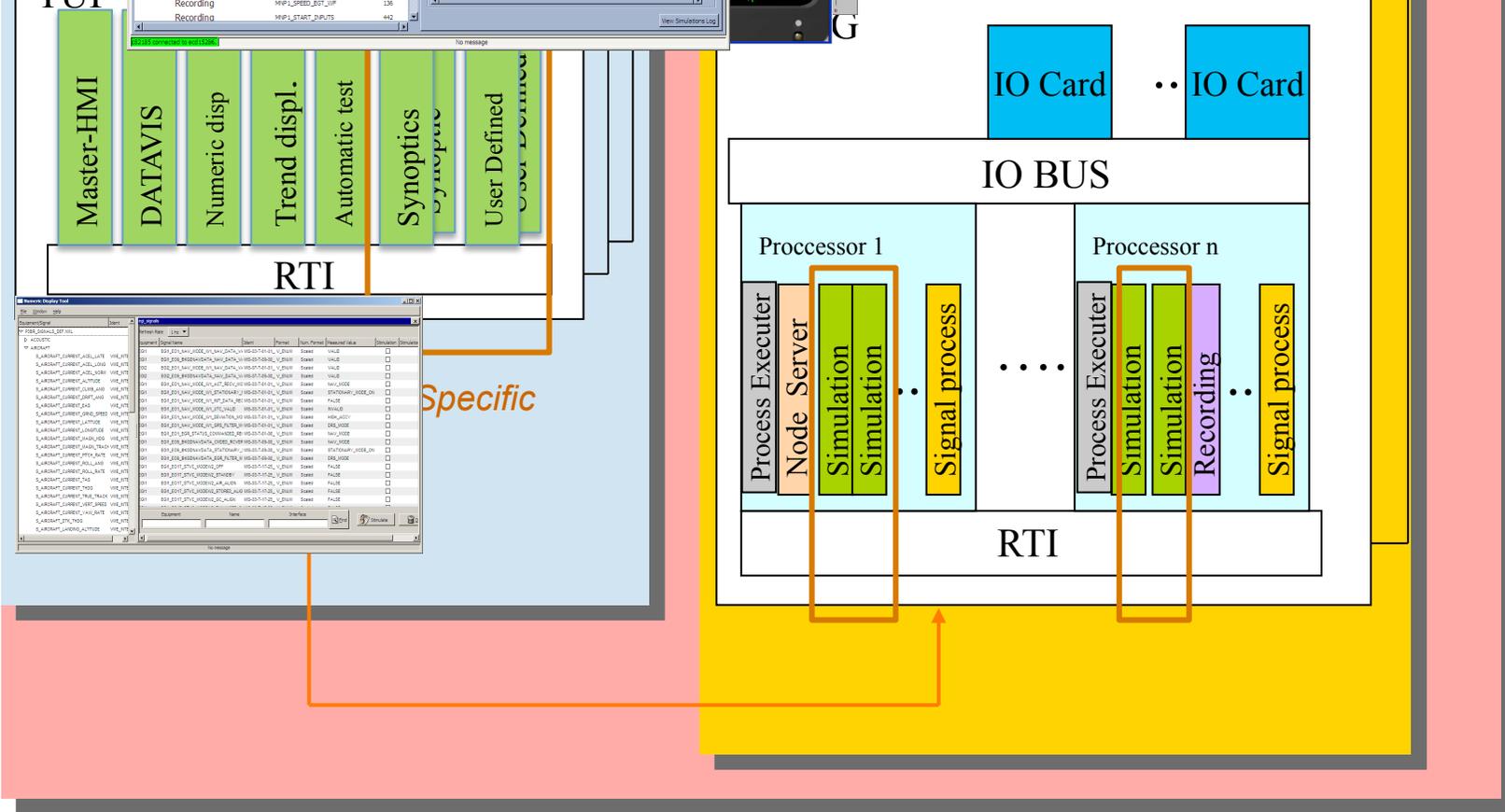
Buttons: Bus Definition, Recording Definition, Sequence Definition, Panels Management, Simulation Starter Definition, Simulations Control, Numeric Display, Sequence Explorer, Data Visualisation, Trend Display.

Server Log:

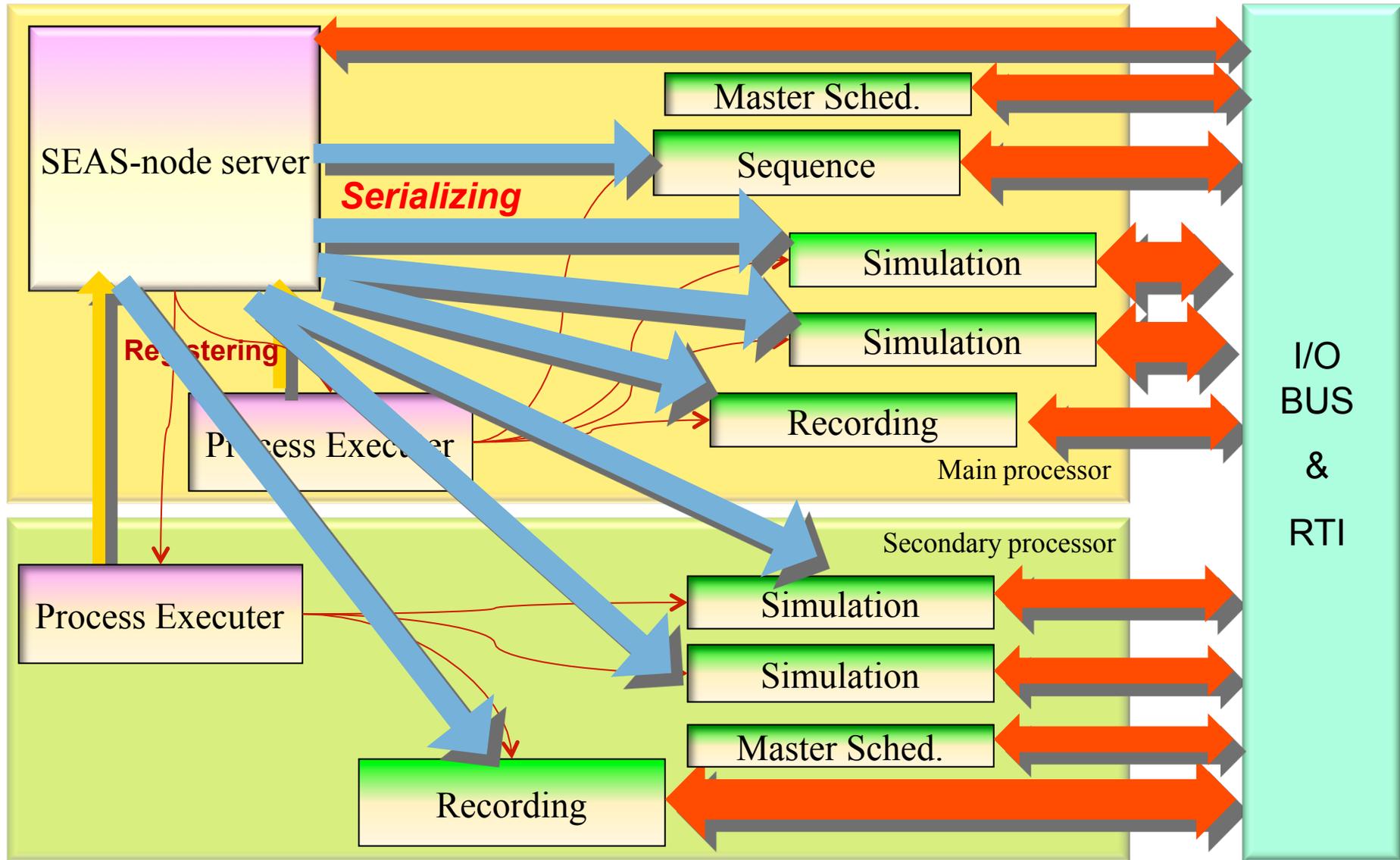

```

    Ping: rtpf-231112009-1510-0506-srv3v-04ATHp3 3.04h (20051204-34)
    Starting test MIP1_VB3C at 2012/01/12 14:36:08
    Number of agents in the test: 707
    => User: 1 opened test in 1 seconds.
    
```

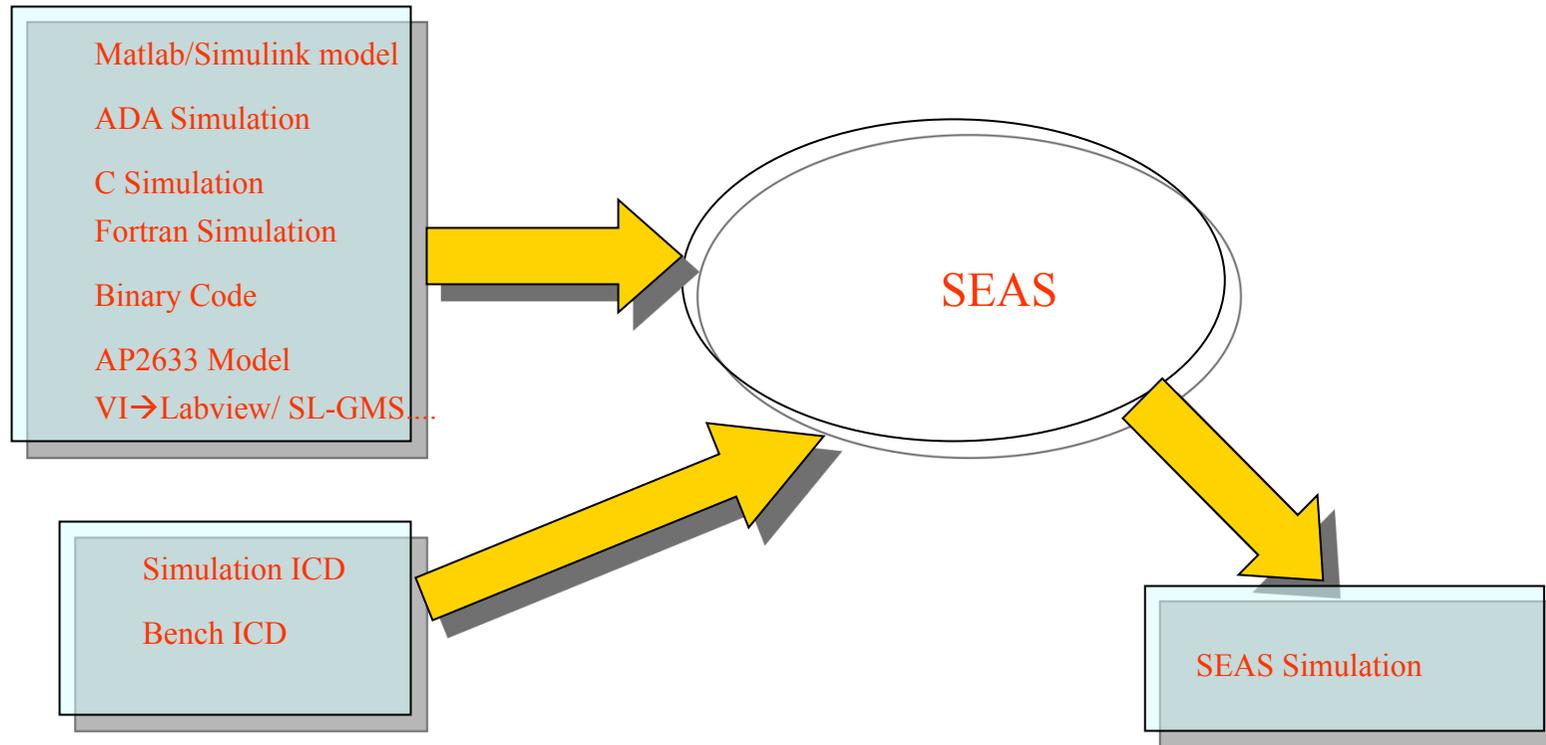
Processing subsystem.



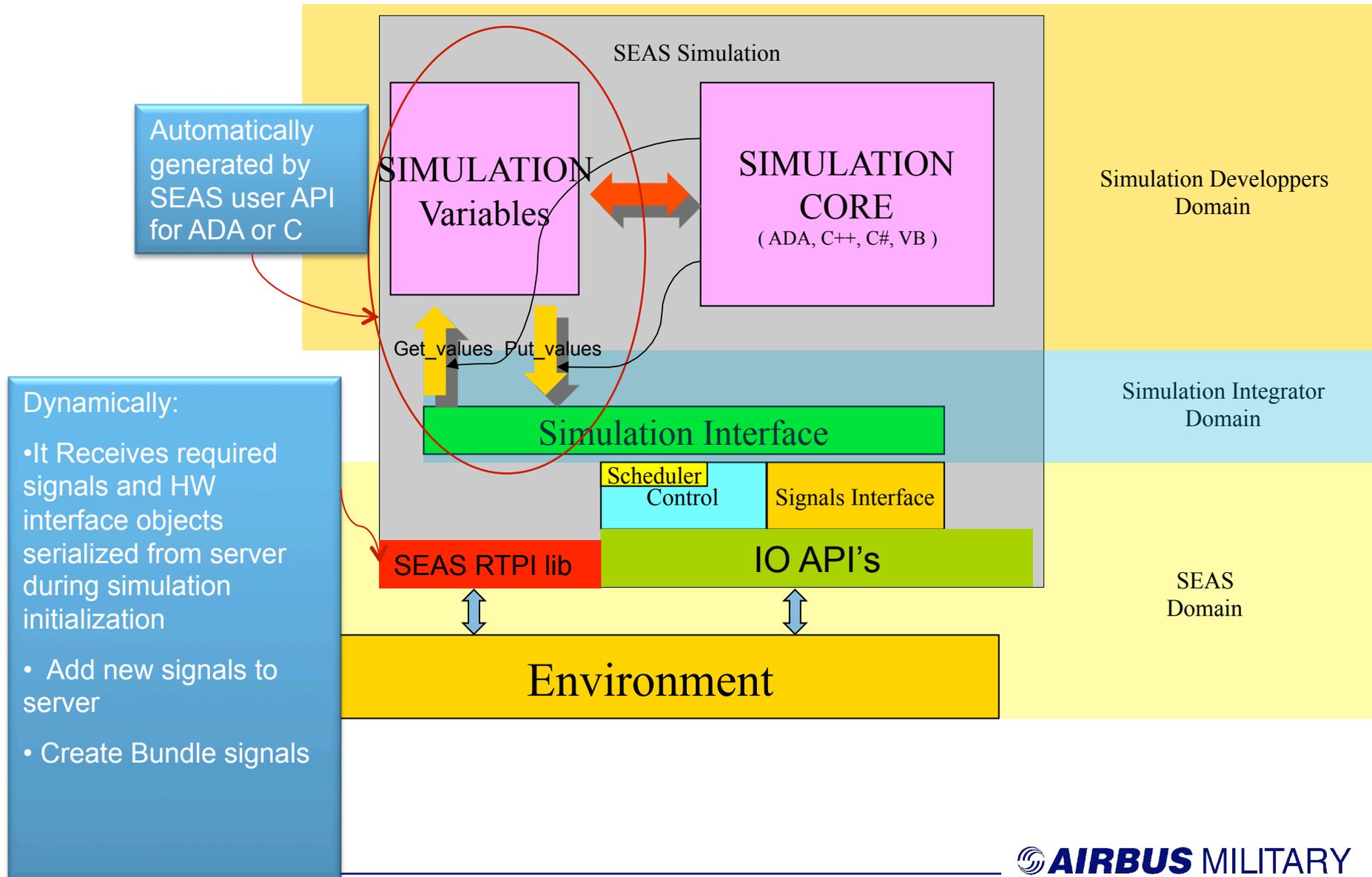
SEAS Signal Process Management



Simulations Build



SEAS Simulations layers



REAL TIME DEVELOPMENT

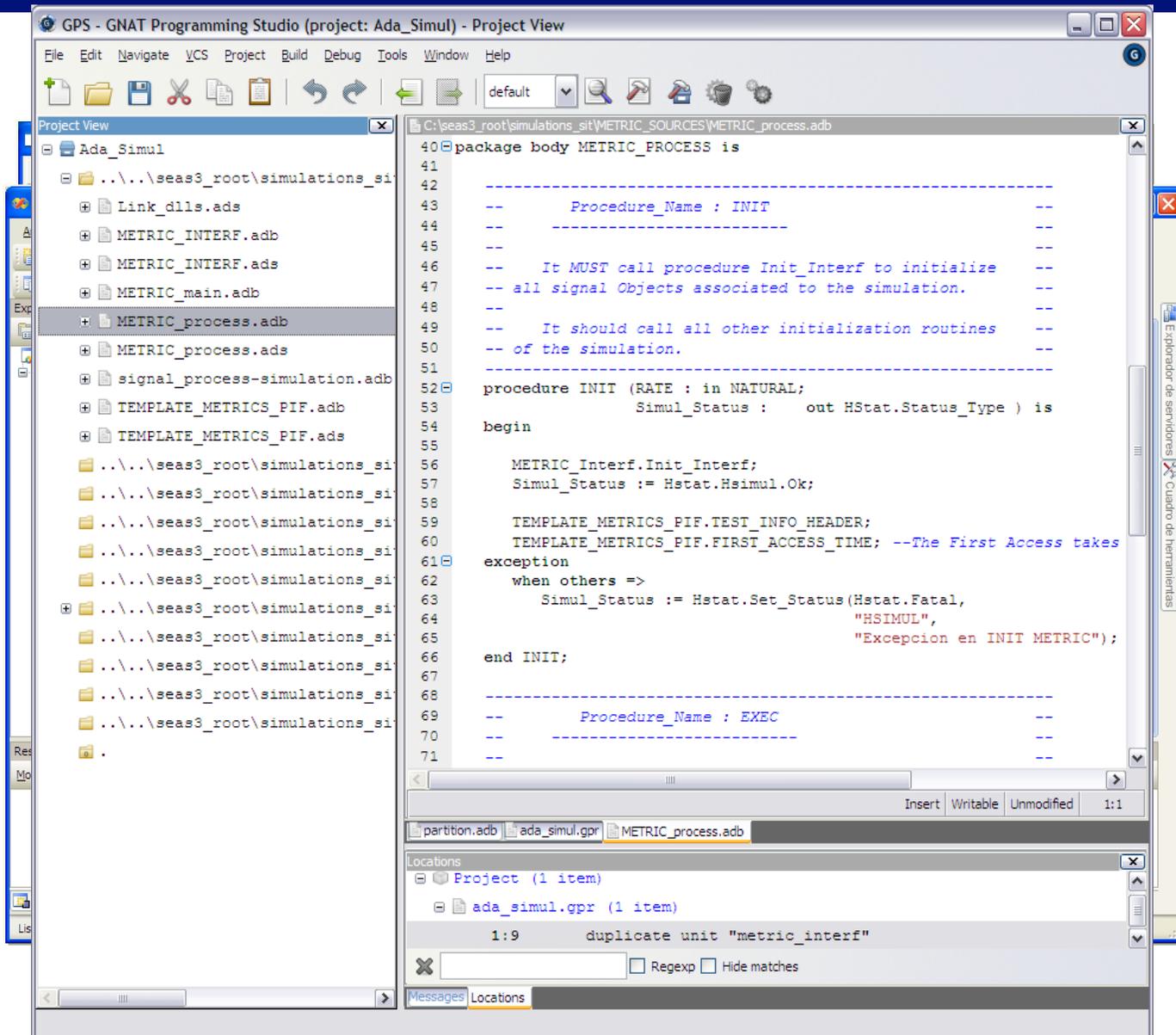
The screenshot displays the GNAT Programming Studio interface with several overlapping windows. The main window shows the source code for `SBS_A429PCI_Board`, which includes various Ada types and functions. A yellow highlight is placed over the `pragma Import` statement, with the text "Binding package to COTS API's given in C or C++". The console window at the bottom shows the execution of `SLC_CLOSED_STATUS_HWPtr := Hsign.Signal.Signal_Simple.GetHwAccess(SLC_CLOSED_S...` and `STATUS_ident);`. The text "Associate Bench signals" is visible in the upper right area of the screenshot.

SEAS Simulation IDE

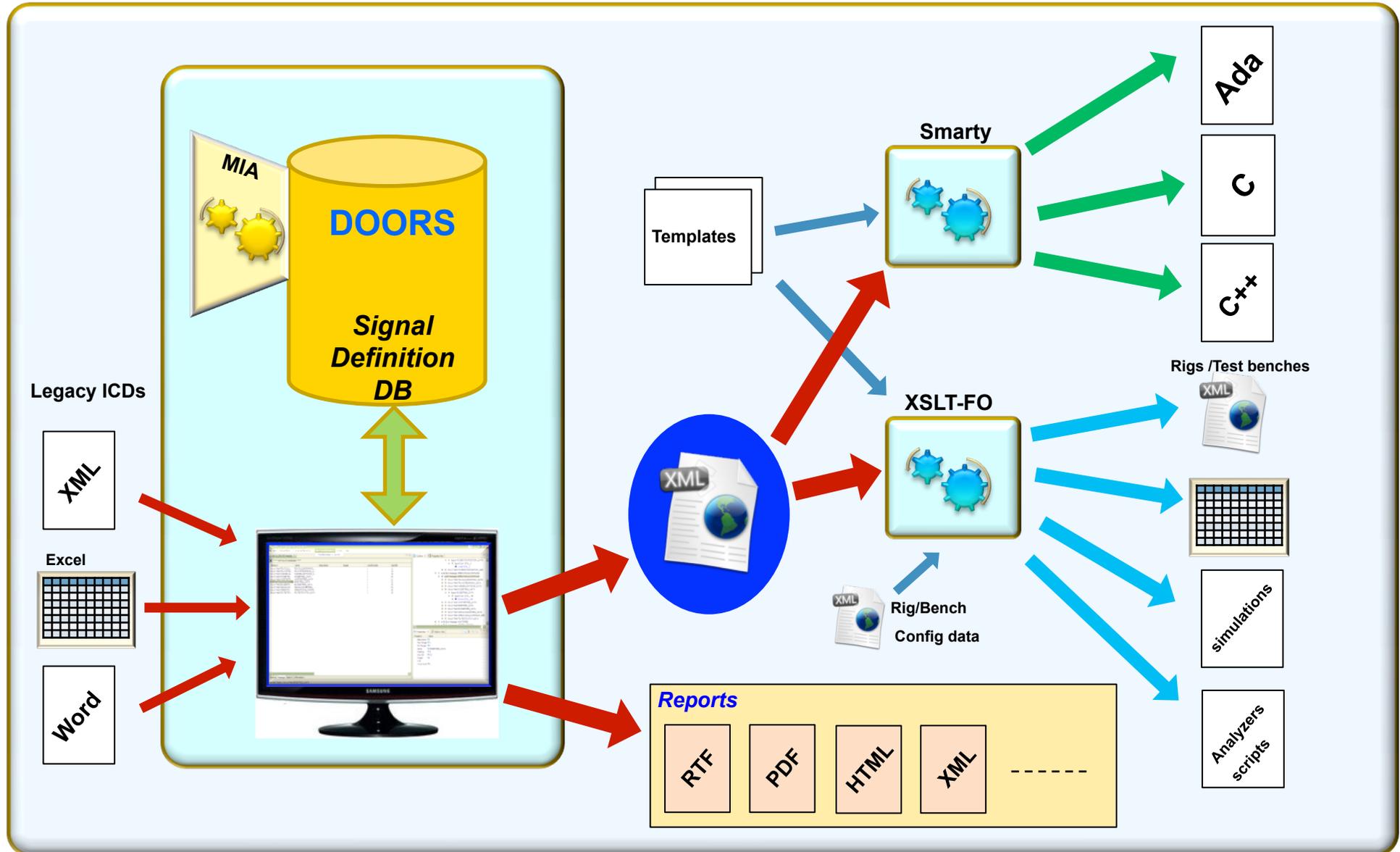
SEAS connect to native IDE for simulations and specific test tools

For Ada and C Code, generating a GPS project and launching GPS

... Or for Visual C source code, generating a Visual C project and launching Visual Studio Application

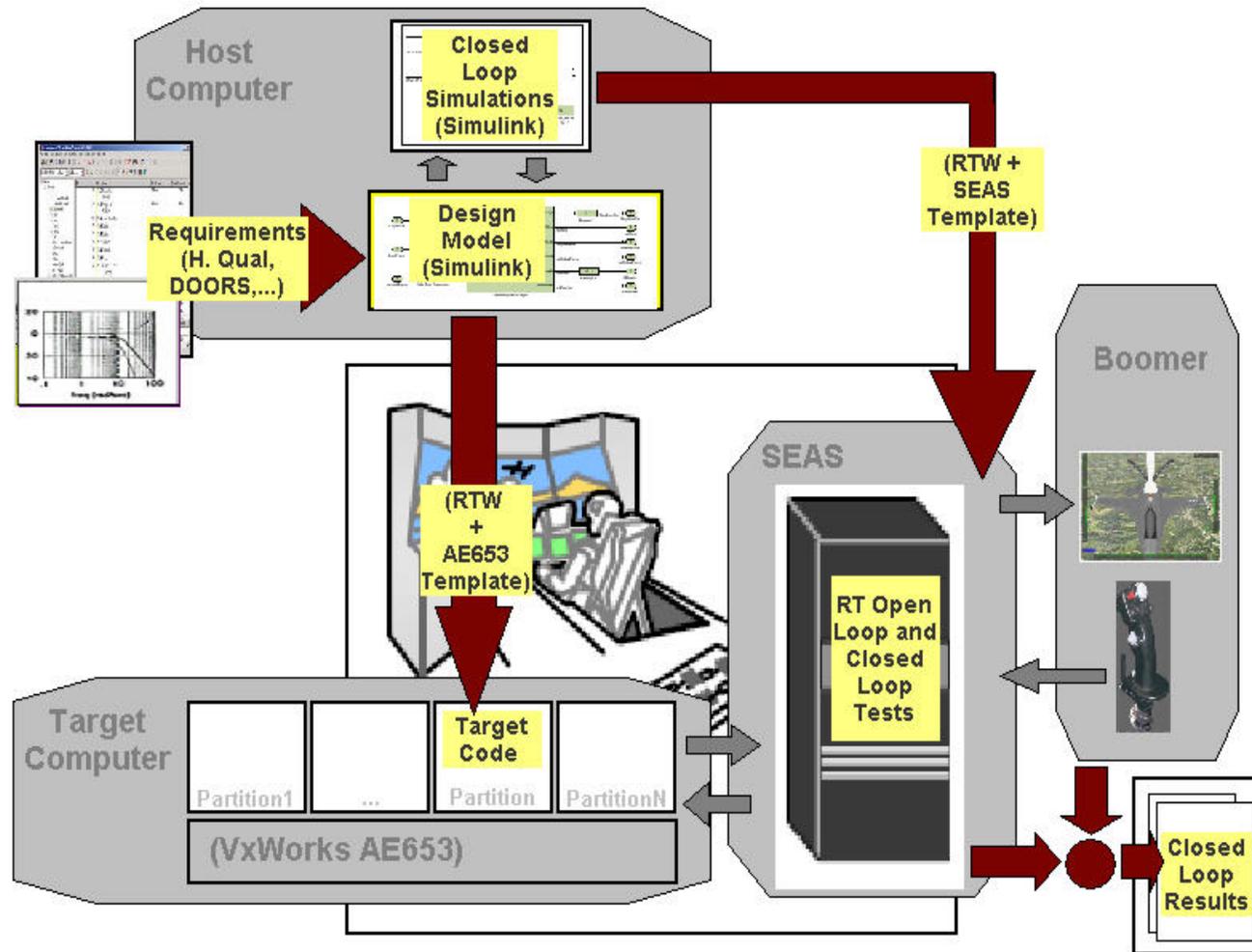


A/C ICD's Management

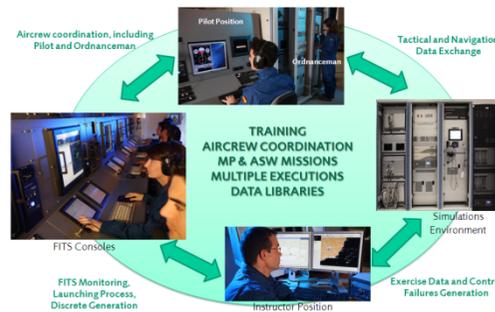


Matlab Simulink models to SEAS Route

ARBS application



Test Facilities



•The maturity, robustness and reliability of the system have been demonstrated throughout hundreds of test facilities in use with this common test environment including Engineering Simulators for Aircraft Refuelling Boom System, System Integration Benches for Multirole Tanker Aircrafts, A400M, Lights&Medium Transport Aircraft, Full Integrated Tactical Systems and Aircraft Interface Modules for Final Assembly Lines of A400M, Multirole Tanker Aircrafts and L&MT aircrafts.

•Proved with up to 350.000 signals per system integration bench

Conclusion

Test System built around GNATPRO-Suite :

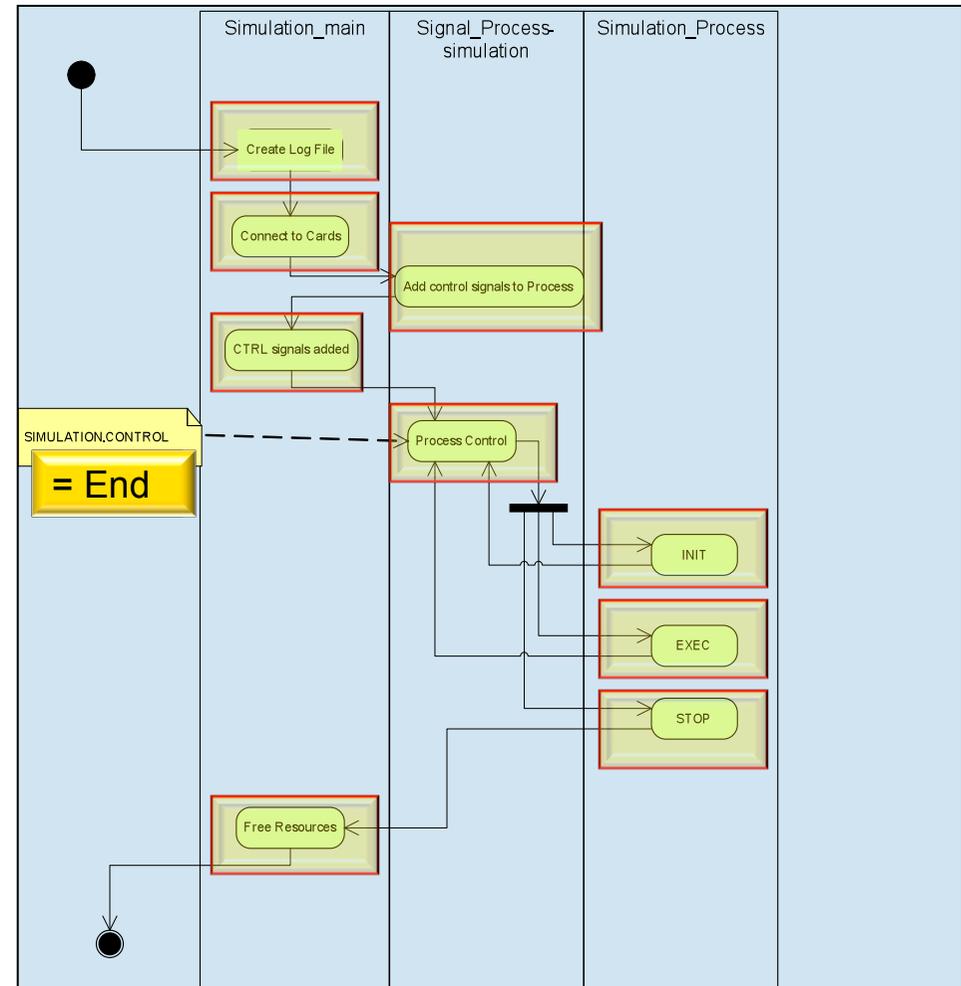
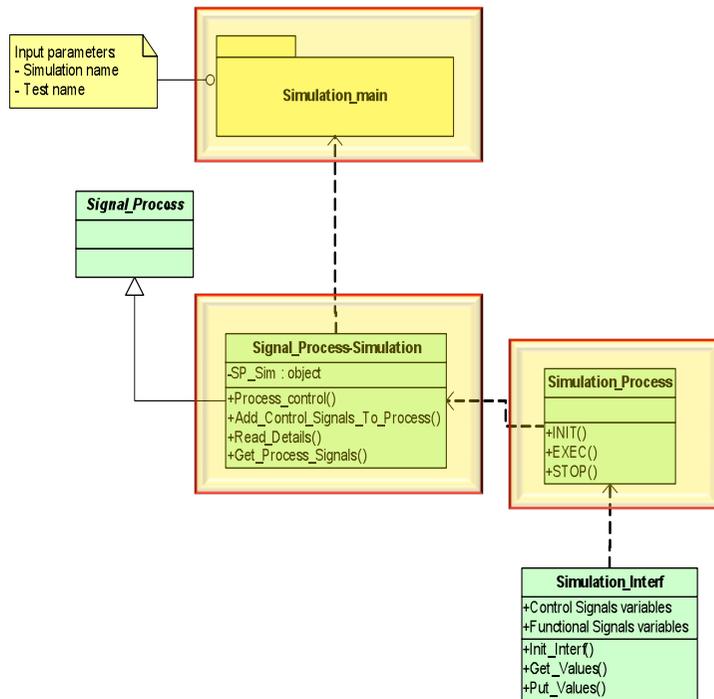
- Ada95 for SEAS Core SW using class wide programming for Processes, Signals, HW Interfaces, Scaling
- Ada95 for Mission System simulations repertoire
- GTKADA for HMI and for special synoptic like mission scenarios, radar display simulations
- XMLADA for test system definitions Bench HW/SW definition
- Distributed Ada for shared memories, RPC , data, types and objects distribution
- GPS to create simulations
- High Flexibility for integration of non Ada SW like FORTRAN,C, C++ API's

We consider using the provided solution by GNATPRO suite, as the core to develop such complex hybrid systems, with large life cycle like test systems for A/C V&V, offered big advantages.

THANK YOU FOR YOUR ATTENTION



Seas Simulation class and activity diagram



Seas Signal class

